

### **ABSTRACT OF THE DISCLOSURE**

An echo cancellation circuit includes delay units at its input unit and output unit in order to adjust the delay caused by a FIFO. The delay times of the delay units are trained using a training signal, such the REVERB signal, that is a system initializing signal. In addition, an upstream pilot tone is used to tune the delay time. The performance of the echo cancellation circuit is optimized according to the device and the method described.

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